INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

	Complete If Known	
Application Number	10/684,793	
Filing Date	October 13, 2003	
First Named Inventor	Yervant Zorian	
Art Unit	2138	
Examiner Name	Nguyen, Steve	

Sheet	1	of	1	Attorney Docket Number	4640P020

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
(init)		Monica Lobetti Bodoni, et al., "An Effective Distributed BIST Architecture for RAMs", IEEE European Test Workshop, 2000 (pages 1-6)	
(init)		Chauchin Su, et al., "A Computer Aided Engineering System for Memory BIST", DAC, 2001 (pages 1-4)	
(init)		Yervant Zorian, et al., "Embedded-Memory Test and Repair: Infrastructure IP for SoC Yield", IEEE CS and IEEE CASS, May-June 2003 (pages 58-66)	

Examiner Signature	*(signature)*	Date Considered	8/16/07

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

[1]Applicant's unique citation designation number. [2]Applicant is to place a check mark here if English language Translation is attached.